



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Zhiqiang Wu, et al. Docket No: TI-29922.1
Serial No: 10/622,052 Conf. No: 2274
Examiner: Edward Joseph Wojciechowicz Art Unit: 2815
Filed: 07/17/2003
For: TRANSISTOR CIRCUIT WITH VARYING RESISTANCE LIGHTLY DOPED DIFFUSED
REGIONS FOR ELECTROSTATIC DISCHARGE ("ESD") PROTECTION

AMENDMENT UNDER 37 C.F.R. § 1.111

Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(a)

I hereby certify that the above correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on 8-2-04.


Ann Trent

Dear Sir:

Responsive to the Office Action mailed April 20, 2004, in connection with the above identified application, Applicants respectfully submit the following amendments and remarks.